Docket No.: 67161-049 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Fumitoshi YAMAMOTO : Confirmation Number:

Serial No.: Group Art Unit:

Filed: September 09, 2003 : Examiner: Unknown

For: SEMICONDUCTOR DEVICE WITH SURGE PROTECTION CIRCUIT

## INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of Japanese Patent Laying-Open Nos. 5-206385 and 56-19657 are discussed in the present specification.

Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including

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Respectfully submitted,

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Date: September 9, 2003

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					APPLICANT Fumitoshi YAMAMOTO					
(PTO-1449)					FILING DATE GROUP September 09, 2003					
				U.S. PATENT	DOCUMENTS					
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	1							Yes	No	
		T	JP 5-206385	08/13/1993	NEC CORP			(Japan w/English Abstract)		
			JP 56-19657	02/24/1981	MITSUBISHI ELECTRIC CORP			(Japan w/English Abstract)		
			JP 58-74081	05/04/1983	NEC CORP			(Japan w/English Abstract)		
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